



**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Application No.: 10/054,653 Confirmation No.: 9448  
First Named Inventor: Bulucea, Constantin Filing Date: 18 January 2002  
Group Art Unit: 2814 Examiner: Farahani, D.  
Atty. Docket No.: NS-5127 US  
Title: Gate-Enhanced Junction Varactor With Gradual Capacitance Variation  
Assignee(s): National Semiconductor Corporation

---

Mountain View, California  
18 January 2006

**MAIL STOP ISSUE FEE  
COMMISSIONER FOR PATENTS  
PO Box 1450  
Alexandria, Virginia 22313-1450**

**AMENDMENT TO TEXT UNDER 37 CFR 1.312**

Sir:

Responsive to the Notices of Allowance and Allowability mailed 18 October 2005 and under the provisions of 37 CFR 1.312, please amend the above patent application as described below.

Ronald J. Meetin  
Attorney at Law  
210 Central Avenue  
Mountain View, CA  
94043-4869  
Tel.: 650-964-9767  
Fax: 650-964-9779